

## SEARCHED

Class	Sub.	Date	Exmr.
703	19	11-26	Dmc
703	28	11-27	
709	104	11-27	
709	218	12-3	
	219		
	220		
	221		
	202		
	107		
	400		
	314		
Updated		6-19-03	Dmc
716	1	1-14-04	Dmc
	6		
	6		
	18		
703	19	1-14-04	Dmc
1	28		

## INTERFERENCE SEARCHED

Class	Sub.	Date	Exmr.

## SEARCH NOTES (INCLUDING SEARCH STRATEGY)

	Date	Exmr.
Clock Synchronization hardware description language	11-26	Dmc
Circuit simulator		
Simulation module		
thread		
token		
Verilog		
VHDL & clock		
IEEE search		
Synchronization		
hardware		
simulation		
Semaphore		
halting execution	11-27	
distributed		
simulation	12-4	
Simulation module		
Synchronization		
points		
Simulation		
clock domain		
Synchronization		
ACM		
Synchronization		
Simulation		
clock	12-7	
Device under test		
DUT Test bench		
Clock cycles	6-19	Dmc
Simulation		
Execution		
time	1-13-04	Dmc
EAST		
VERILOG VHDL DUT		
Clock credit		
Clock token		
Clock domain		
Simulation module	1-13-04	Dmc
Factors		
Clock credit		
Execution time		
Simulation manager		

(RIGHT OUTSIDE)